

CONCURRENT DUAL-BAND RECEIVER ARCHITECTURE

ABSTRACT

5 The present invention discloses an architecture for a concurrent dual-band high-frequency receiver. The invention combines a concurrent dual-band front-end subsystem having a dual-band antenna, dual-band pre-amplifier filter and concurrent dual-band LNA with a novel image rejection downconverter to provide the functions of a typical receiver, including reception, amplification and downconversion of a signal in two discrete desired frequency bands simultaneously.

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